

In the Claims:

Please cancel claim 1, without prejudice, and amend claims 2 and 5-8 as follows:

1. (Cancelled)

2. (Currently Amended) A display control device according to claim 15, wherein said CRT control unit and said LCD control unit respectively receive video data for a transfer object which are stored on a video memory in accordance with readout timing generated from the second clock.

3. (Original) A display control device according to claim 2, wherein said CRT control unit and said LCD control unit transfer the video data at such timing that the same picture is displayed substantially simultaneously on the CRT display and on the LCD display.

4. (Original) A display control device according to claim 3, wherein the readout timing is generated synchronizing with a display cycle of the LCD display,

said LCD control unit receives the video data for the transfer object that are stored on the video memory each time the readout timing is generated, and

said CRT control unit receives the video data for the transfer object which are stored on the video memory only in a case where the readout timing further synchronizing

with a display cycle of the CRT display is generated.

5. (Currently Amended) A display control device ~~according to claim 1,~~  
comprising:

a cathode ray tube (CRT) control unit for transferring, to a CRT display, a CRT transfer clock signal, a video data signal and a synchronous signal in accordance with the CRT transfer clock signal generated from a first clock signal having a constant and stable cycle;

a liquid crystal display (LCD) control unit for transferring, to a LCD display, a LCD transfer clock signal, a video data signal and a synchronous signal in accordance with the LCD transfer clock signal generated from a second clock signal as a spread spectrum clocking signal generated based on the first clock signal; and

~~further comprising~~ a monitoring unit monitoring a transfer quantity of the video data per unit time and outputting a signal for controlling a width of the spread spectrum of the second clock in accordance with the transfer quantity of the video data.

6. (Currently Amended) A display control device according to claim 15, wherein said display control device is structured, into one chip, together with said video memory for storing the video data for the transfer object.

7. (Currently Amended) A display control device according to claim

45, further comprising a digital visual interface,

wherein in the case of displaying the picture on the CRT display, the video data from said CRT control unit are transferred to the CRT display via said digital visual interface, and

in the case of displaying the picture on the liquid crystal display, the video data from said LCD control unit, the horizontal/vertical synchronous signals and the second clock are transferred to the liquid crystal display via said digital visual interface.

8. (Currently Amended) A display control device according to claim

45, wherein said display control device is structured, into one chip, together with a chip connecting a central processing unit (CPU), a main memory and an extension bus to each other, and serving as a bridge for the data therebetween.